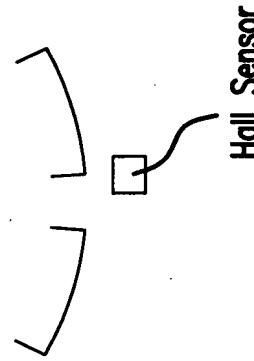


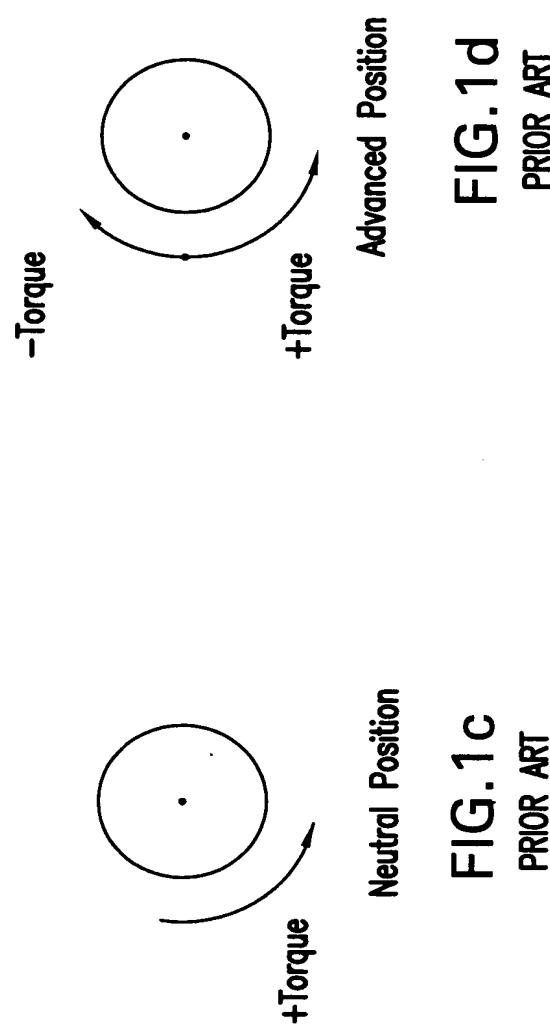
Neutral Position

FIG. 1 a
PRIOR ART



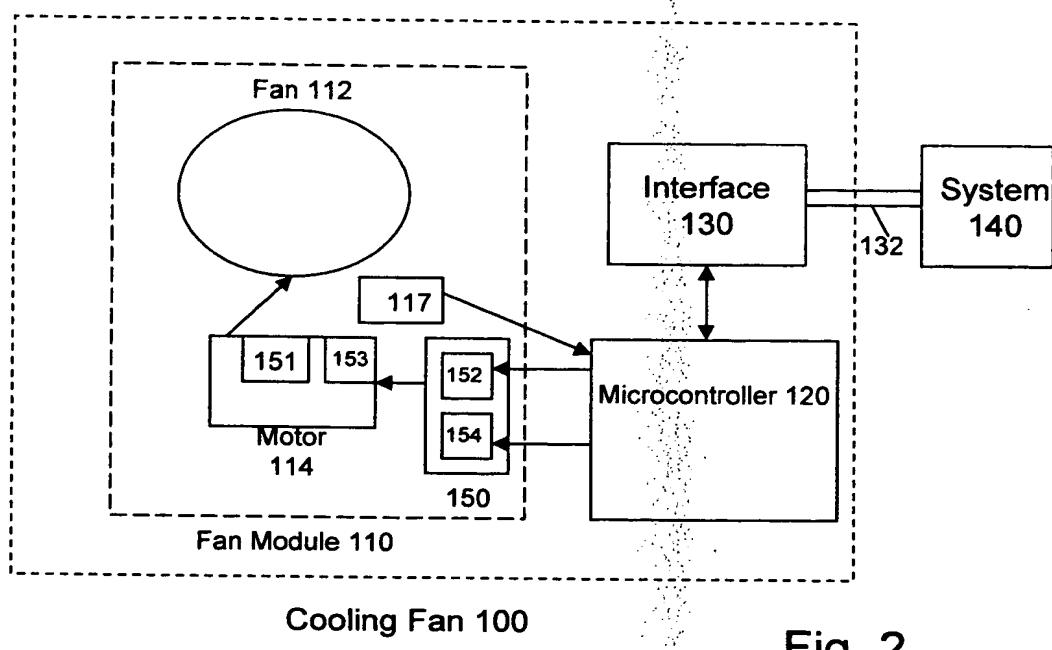
Advanced Position

FIG. 1 b
PRIOR ART



Neutral Position
FIG. 1 c
PRIOR ART

Advanced Position
FIG. 1 d
PRIOR ART



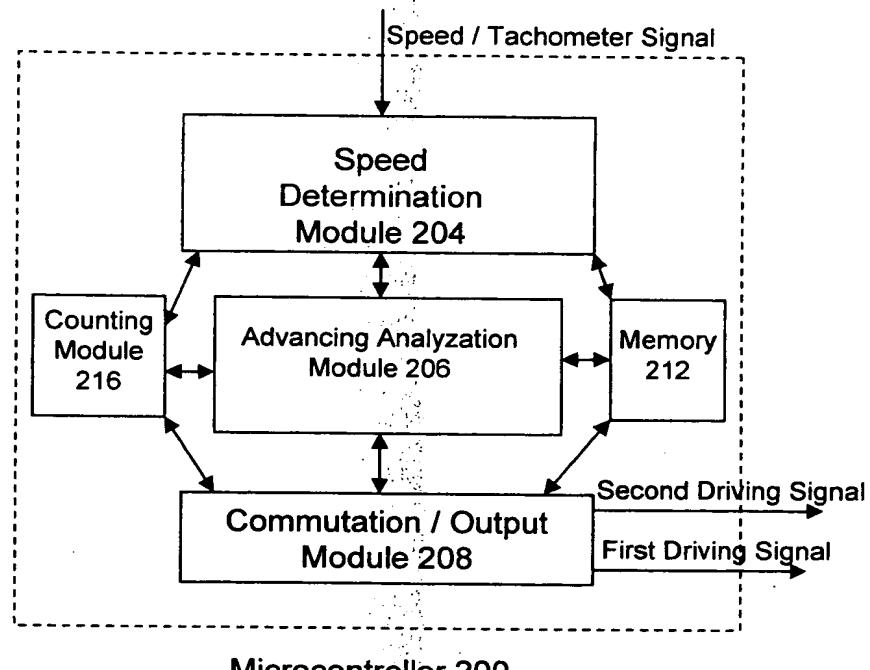
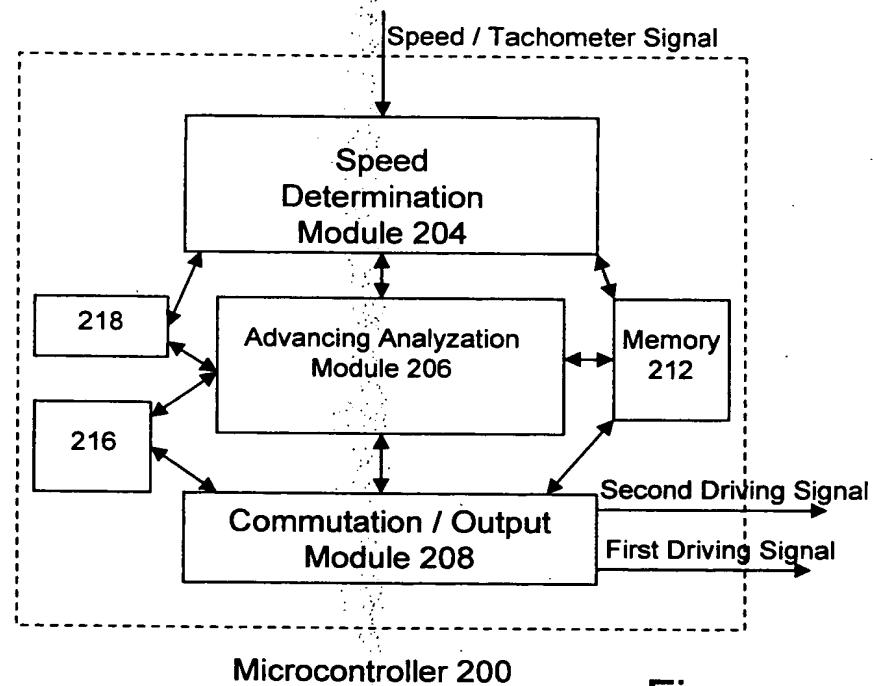


Fig.
3(a)



Microcontroller 200

Fig.
3(b)

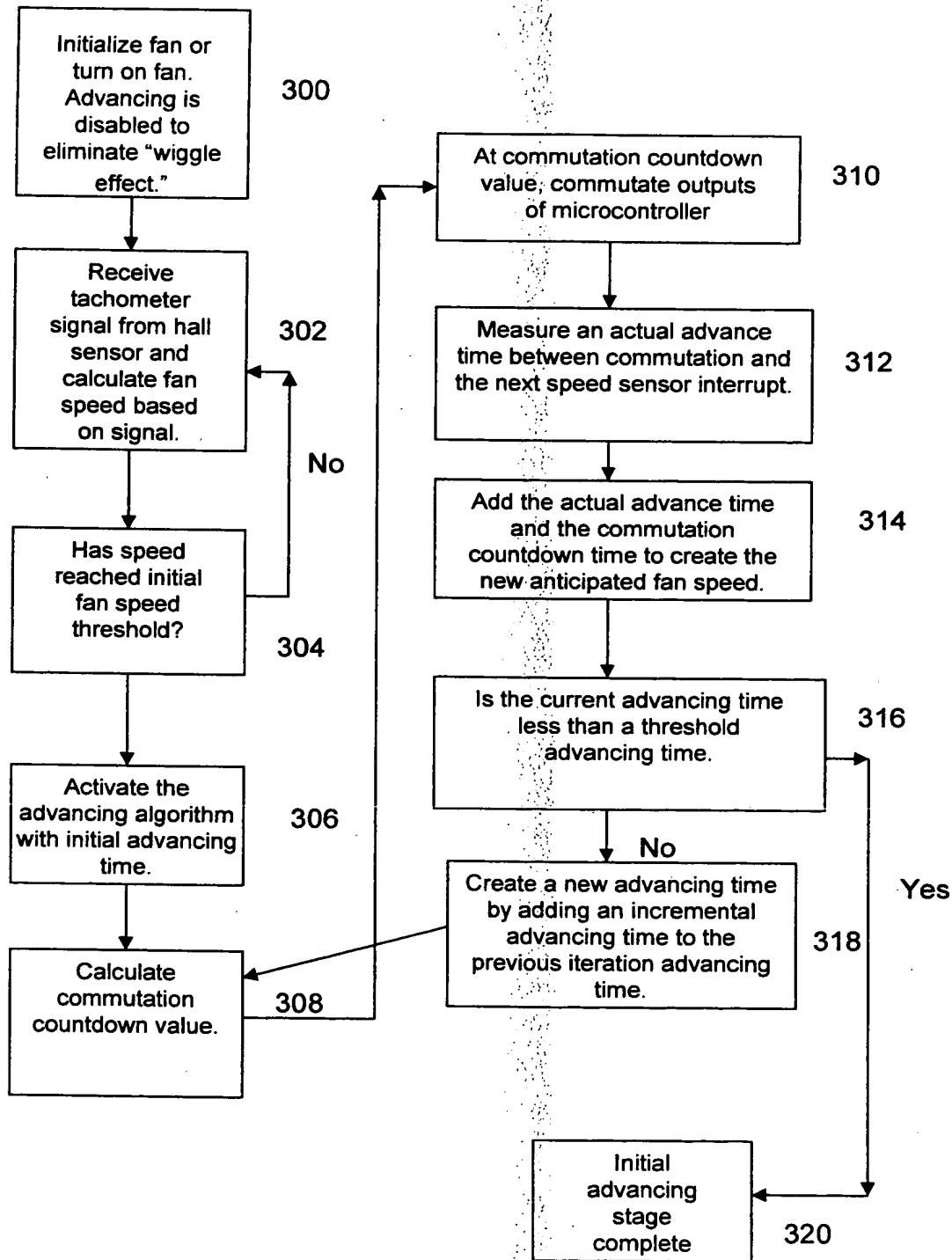


Fig. 4(a)

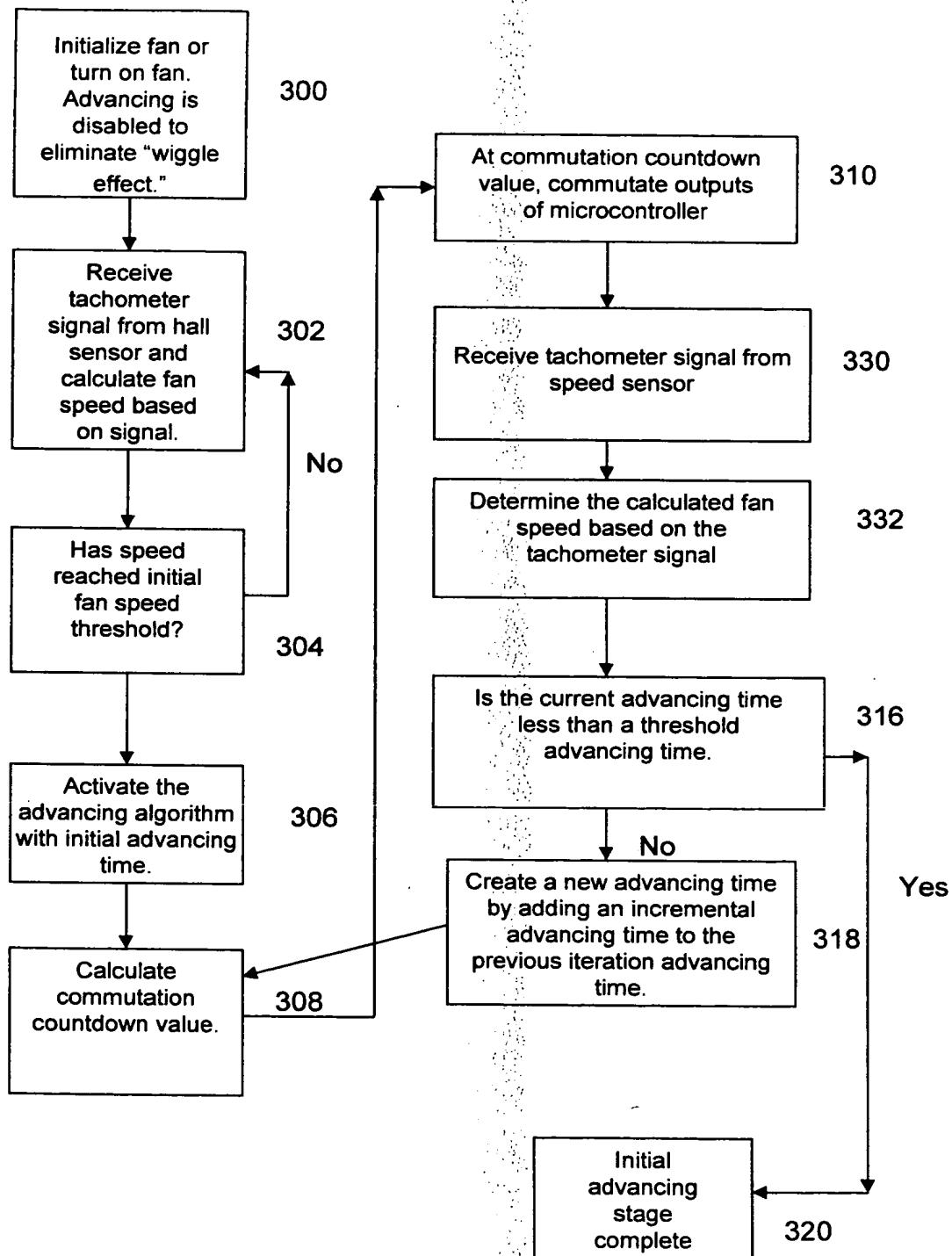


Fig. 4(b)

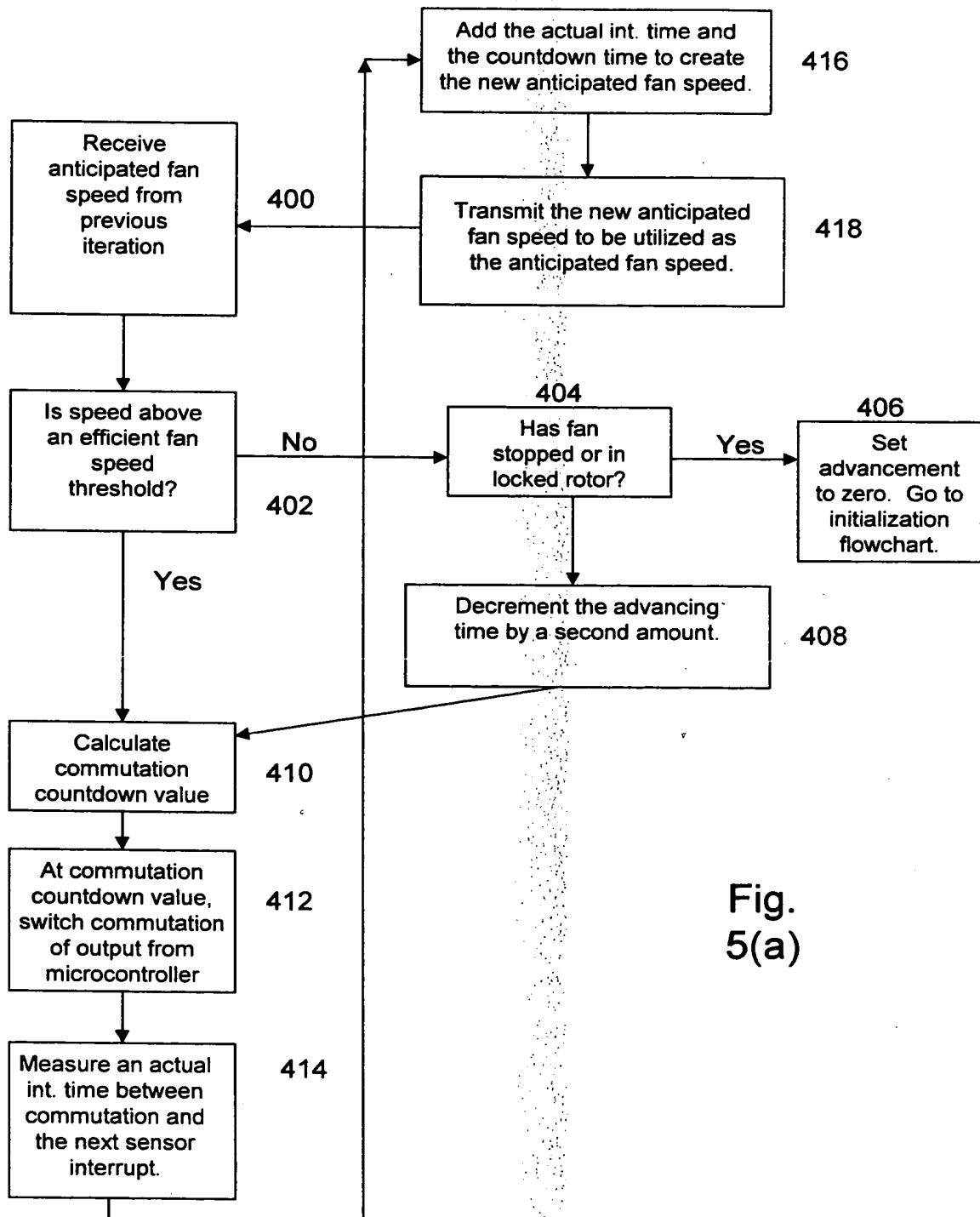


Fig.
5(a)

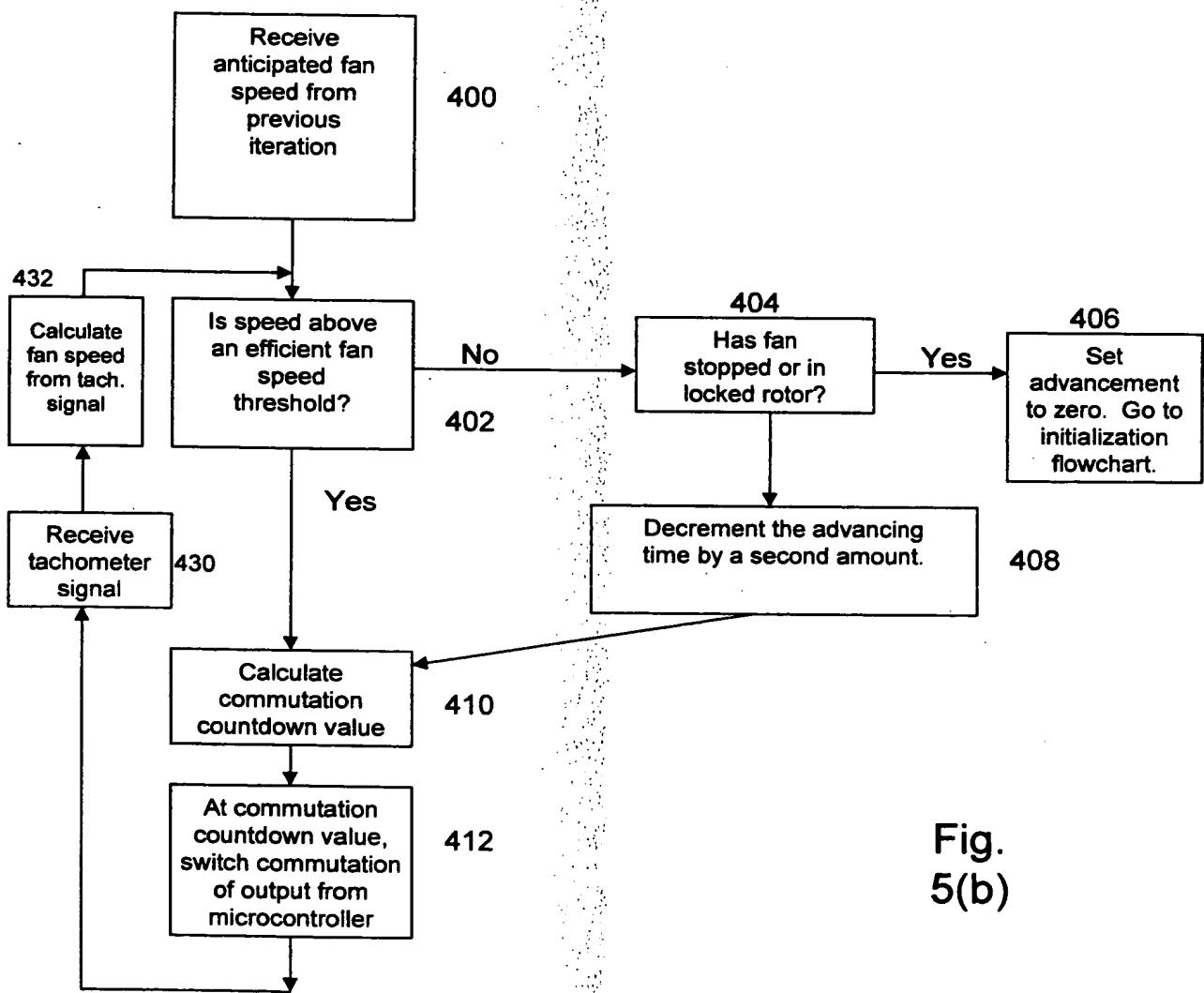


Fig.
5(b)